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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/821,370

Filed: April 9, 2004

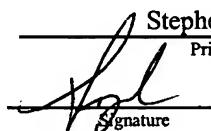
Inventor(s):

Landin et al.

§ Examiner: Eland, Shawn  
§ Group/Art Unit: 2188  
§ Atty. Dkt. No: 5181-99901

Title: **MULTI-NODE  
COMPUTER SYSTEM  
EMPLOYING MULTIPLE  
MEMORY RESPONSE  
STATES**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

  
Stephen J. Curran  
Printed Name  
  
3/22/07  
Signature Date

**AMENDMENT; RESPONSE TO OFFICE ACTION OF  
December 22, 2006**

**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of December 22, 2006, to further highlight why the application is in condition for allowance.